

## HIERARCHICAL MODELING FOR VLSI CIRCUIT TESTING%0A

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Our high-level circuit and modeling techniques are developed in Chapter 2, along with a suitable notation for describing the behavior of hierarchical systems.

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**Very Large Scale Integration - Wikipedia**

Very-large-scale integration (VLSI) is the process of creating an integrated circuit (IC) by combining millions of transistors or devices into a single chip. VLSI began in the 1970s when complex semiconductor and communication technologies were being developed.

**ABSTRACT This paper describes the hierarchical design ...**

This paper describes the hierarchical design process for VLSI circuits and discusses the potential benefits and disadvantages. 1. INTRODUCTION Over the past decade software designers have learned to cope with increasingly complex programs. In order to deal with this complexity a structured programming methodology has been developed. The advent of VLSI technology has brought similar

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